

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. KOBAYASHI, et al.

Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial
No. 10/206,982, filed July 30, 2002

Filed: On even date herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
INCLUDING FIRST, SECOND AND THIRD GATES
(As Amended)

Art Group of Parent: 2818

Examiner of Parent: V. Nguyen

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 30, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Title and Specification

Amendment to the Claims

Remarks are included following the amendments.